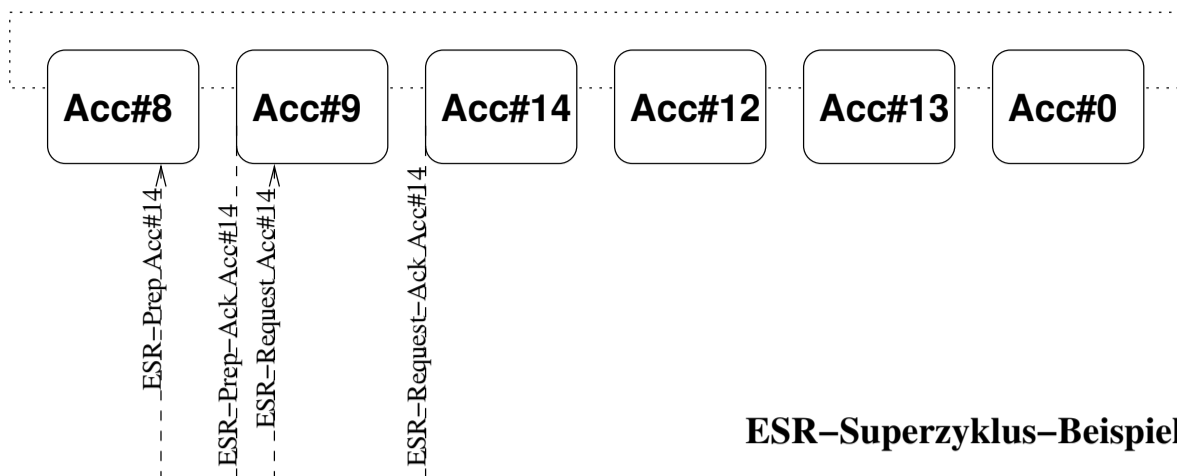
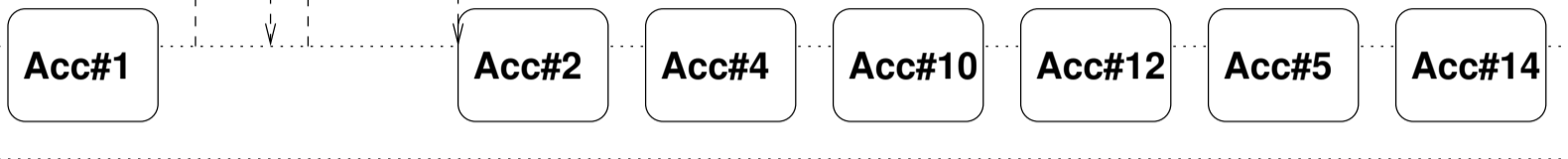


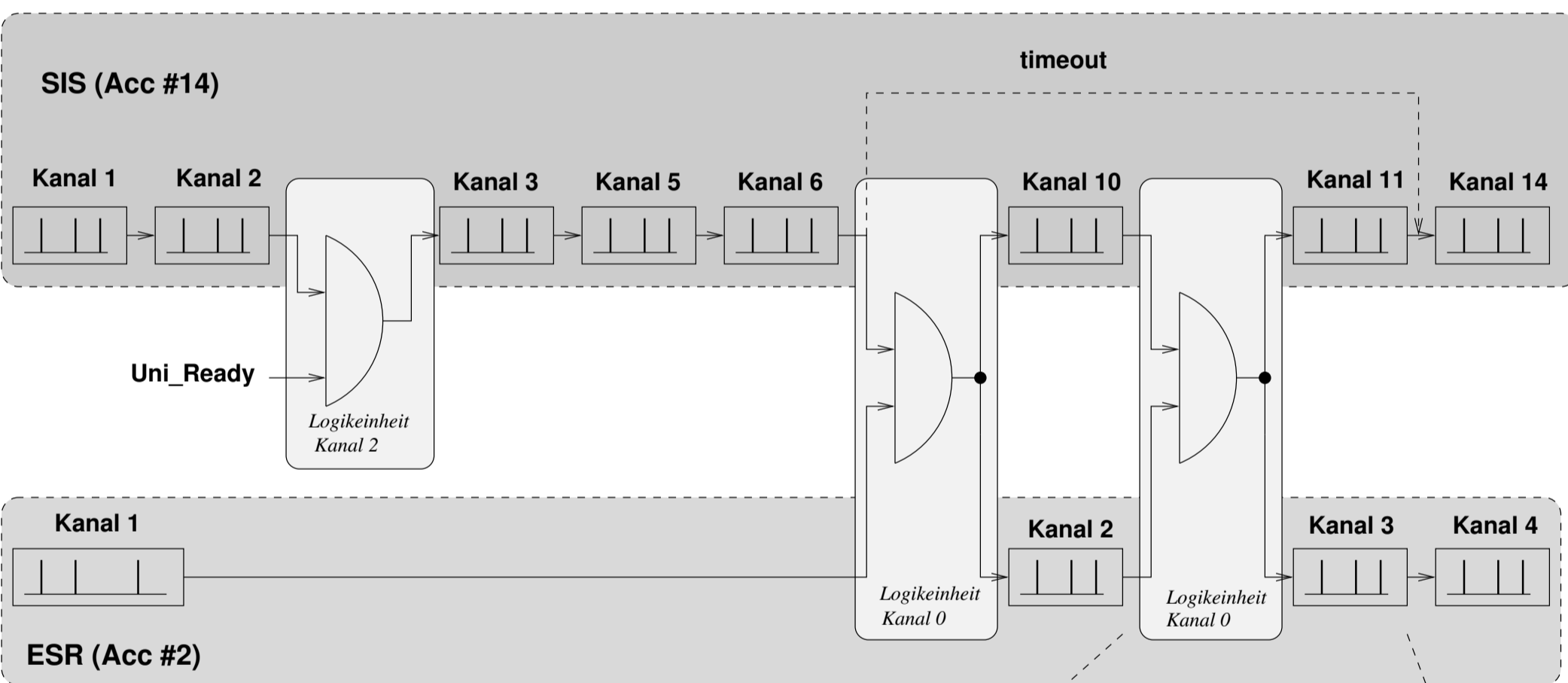
SIS-Superzyklus-Beispiel



ESR-Superzyklus-Beispiel



Kanalsynchronisierung SIS/ESR (H=1 Extraktion)



Belegung der Kanäle der Logikeinheit:

- Kanal 0: Trans_Start_1/2 (61/62)
 Async_Trans (79)
- Kanal 1: Kick_Start_1/2 (49/69)
- Kanal 2: Uni_Ready (38)
- Kanal 3: Spillabbruch
- Kanal 4: Kick_Ready (105)

Extraktion mit HF-Synchronisierung

