

**Spill Abort SCU @ BG2.009 Rack BG2A.F8**

Slot	1	2	3	4	5	6	7	8	9	10
	DIOB Backplane									
	FG902.011	FG902.011	FG902.011	FG902.011	==	==	==	FG902.021	FG900.841	FG900.841
in1	Cave A abort BIO.1 PatchB.8 IN_Reg0.0 In0	Cave M abort 16.1 PatchB.2 IN_Reg0.5 In5							Hardware Interlock FRS	Hardware Interlock Hades
in2	Cave A pause BIO.2 PatchB.9 IN_Reg0.1 In1	Cave M pause 16.2 PatchB.3 IN_Reg0.6 In6								
in3	FRS abort 17.1 PatchA.1 IN_Reg0.2 In2	Hades abort 18.1 PatchB.1 IN_Reg0.7 In7								
in4										
in5										
out	FQ abort 19.1 PatchA.7	FQ reset 19.2 PatchA.8	RF 20.1 PatchA.11	KO 20.2 PatchA.12				Timestamp		

(Timestamp output geht an B1 input des Timingreceivers)

**PATCHPANEL**  
**KABEL**  
**DIOB internal**

**Patch Panel A (selbes Rack)**

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
FRS abort	-	-	-	-	-	FQ abort	FQ reset	-	-	RF	KO	-	-	-	-
<b>17.1</b>	<b>17.2</b>	<b>17.3</b>	<b>17.4</b>	<b>17.5</b>	<b>17.6</b>	<b>19.1</b>	<b>19.2</b>	<b>19.3</b>	<b>19.4</b>	<b>20.1</b>	<b>20.2</b>	<b>20.3</b>	<b>20.4</b>	<b>20.5</b>	<b>20.6</b>

**Patch Panel B (selbes Rack)**

1	2	3	4	5	6	7	8	9	10	11	12
Hades abort	Cave M abort	Cave M pause	-	-	-	-	Cave A abort	Cave A pause	-	-	-
<b>18.1</b>	<b>16.1</b>	<b>16.2</b>	<b>16.3</b>	<b>16.4</b>	<b>16.5</b>	<b>16.6</b>	<b>BIO.1</b>	<b>BIO.2</b>	<b>BIO.3</b>	<b>BIO.4</b>	<b>BIO.5</b>

**Direkt verbunden am Patchpanel (keine Arbitrierung via DIOB)**

<b>Cave M</b>	<b>&lt;-&gt;</b>	<b>RF/KO</b>	<b>Cave M</b>	<b>&lt;-&gt;</b>	<b>FRS</b>
<b>16.3 / B.4</b>		<b>20.3 / A.13</b>	<b>16.5 / B.6</b>		<b>17.2 / A.2</b>
<b>16.4 / B.5</b>		<b>20.4 / A.14</b>	<b>16.6 / B.7</b>		<b>17.3 / A.3</b>

## Andere Enden der Kabel

	<b>Raum</b>	<b>Rack#</b>	<b>Kontakt</b>
CaveA	TH2.005	TH2A	C. Hartmann-Sauter
CaveM	AR1.001	7	C. Hartmann-Sauter
FRS	EX1.026	FRSRMHS2	F. Ameil / S. Pietri
Hades	TH1.004	1	J. Adamczewski-Musch
FQ	BG2.001	SVE11-SIS	U. Clausen
KO/RF	BG1.016	"KO-Rack"	B. Zipfel